



Micron Drives DDR5 Adoption With Technology Enablement Program

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Ecosystem of innovators to bring to market the most technologically advanced DRAM for computing platforms

BOISE, Idaho, July 14, 2020 (GLOBE NEWSWIRE) -- Micron Technology, Inc. (Nasdaq: MU), today announced a comprehensive enablement program which will provide early access to technical resources, products and ecosystem partners. The Technology Enablement Program will aid in the design, development and qualification of next-generation computing platforms that use DDR5, the most technologically advanced DRAM available.

Today's news, alongside the ratification of the [JEDEC DDR5 standard](#), builds on Micron's January announcement [of DDR5 RDIMM samples](#) and brings the industry one step closer to unlocking the value in next-generation, data-centric applications. Companies joining Micron in the DDR5 Technology Enablement Program include Cadence, Montage, Rambus, Renesas and Synopsys.

DDR5 delivers improvements to performance, density and reliability at a time when modern data centers need to feed rapidly growing processor core counts with memory bandwidth, and meet increasing customer demands for reliability, availability and serviceability. DDR5 will offer greater than twice the effective bandwidth when compared to its predecessor DDR4, helping relieve this bandwidth-per-core crunch and enabling high performance and improved power management in a wide variety of applications.

"Micron has been driving deep technical engagements with the world's largest server and hyperscale companies to help them better understand how DDR5 can benefit their unique workloads," said Tom Eby, senior vice president and general manager of the Compute & Networking Business Unit at Micron. "We're proud to now extend that collaboration across the entire ecosystem through our Technology Enablement Program to help accelerate time to market for this critical next generation technology."

Channel partners also play a very important role in any new technology's development and adoption. As part of the DDR5 Technology Enablement Program (TEP), Micron will work alongside channel partners like distributors, value-added resellers and OEMs/ODMs as they bring new and innovative products that use DDR5 to the market.

Teresa Kelley, vice president and general manager of the Consumer Products Group at Micron, stated, "Crucial is working closely with partners and customers at every step of their development process to ensure a smooth transition to DDR5. From power-on through validation and final qualification, Crucial is committed to providing DDR5 memory solutions for all our channel customers, whether it be for data center, consumer or gaming applications."

Qualified partners that enroll in the program can leverage Micron's world-class collaboration, quality and support and enjoy certain other benefits including access to the following:

- Certain DDR5 components and modules
- New DDR5 products as they become available
- Technical resources including data sheets, electrical and thermal models to aid in product development and evaluation as well as consultation on signal integrity and other technical support
- Other ecosystem partners who can aid in chip- and system-level design

Enablement Program Partner Quotes

Rishi Chugh, vice president of Product Marketing, IP Group, Cadence

"DDR5 is primarily about density, making it particularly well-suited for enterprise, cloud and big data applications. Cadence has been collaborating with Micron on DDR5 DRAM for more than two years, enabling Micron DDR5 DRAM market adoption with over 15 licensed customers for Cadence's DDR5 IP."

Geof Findley, vice president of Sales and Business Development, Montage Technology

"As a leading DDR4 memory logic vendor, Montage Technology is excited to partner with Micron on the launch of high-performance DDR5 RDIMM and LRDIMM. We are pleased to offer a comprehensive portfolio of low-power DDR5 logic devices — including DDR5 RCD, DDR5 DB, DDR5 SPD-Hub, DDR5 PMIC and DDR5 Temperature Sensor — and to help drive quick industry adoption of DDR5 memory."

Chien-Hsin Lee, vice president and general manager, Integrated Circuits, Rambus

"As the first to deliver a silicon-proven DDR5 memory interface chipset for DDR5 DIMMs, Rambus is excited to enable the DDR5 ecosystem with our RCD and data buffer chips. We are pleased to work with leaders like Micron to deliver high-quality solutions that accelerate the time-to-market and adoption of DDR5 memory in leading-edge data center and cloud systems."

Rami Sethi, vice president of Data Center Business Division, Renesas

"Renesas has been at the forefront of delivering a full suite of silicon solutions to enable all varieties of DDR5 DIMMs. The strong partnerships cultivated over many years with our ecosystem partners and customers such as Micron are essential to bringing the next generation of computing architectures to the industry."

John Koeter, senior vice president of marketing and strategy for IP, Synopsys

"As the leading provider of DDR IP, Synopsys has been collaborating with Micron to help mutual customers achieve silicon success through many generations of DDR standards, including DDR5. Synopsys' silicon-proven DesignWare DDR5 IP with differentiated features such as firmware-based

training is validated using Micron's DDR5 DRAM, offering a low-risk solution. Designers can confidently integrate the DDR controller, PHY, and verification IP solution into their high-performance computing SoCs to meet their data rate, bandwidth efficiency, capacity, and RAS requirements."

To apply to the program, visit www.micron.com/ddr5

Resources

- Press release: [The Next Leap in Data Center Performance Arrives With Micron DDR5](#)
- Video: [Micron DDR5: Offering More Than 2X the Effective Bandwidth](#)
- Video: [Micron DDR5: Next-generation memory transforming data into insight](#)

About Micron Technology, Inc.

We are an industry leader in innovative memory and storage solutions. Through our global brands — Micron® and Crucial® — our broad portfolio of high-performance memory and storage technologies, including DRAM, NAND, NOR Flash and 3D XPoint™ memory, is transforming how the world uses information to enrich life. Backed by more than 40 years of technology leadership, our memory and storage solutions enable disruptive trends, including artificial intelligence, machine learning and autonomous vehicles, in key market segments like data center, networking, automotive, industrial, mobile, graphics and client. Our common stock is traded on the Nasdaq under the MU symbol.

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