



Building Applications on multi-core RISC-V[®] based on Microchip PolarFire[®] SoC – Part II

Yocto and Embedded Linux

10th November 2021 – online

Utilizing the flexibility and high level of integration provided by the PolarFire[®] SoC FPGA device family requires a broad range of expertise. It stretches from RTL development of IP cores for custom interfaces and accelerators and their integration into a system on the hardware side. On the software side, device drivers and application software have to be developed.

This webinar highlights the software related parts of the development process and ends with some basic software running on the system.

A quick overview of the Linux build and deployment process using Yocto is given. Flashing and booting the provided default Yocto binary image is demonstrated and some first steps using the Linux command line are undertaken.

Using this Linux image as a starting point, the implementation of a simple Linux device driver to access the IP-Cores implemented in part one (available on demand) will be outlined in a compressed but comprehensible way.

More details on Cyber Security and how to shield your application against Cyber Attacks will be presented in the third part of this webinar series scheduled for 8th of December 2021.

Speakers: Martin Kellermann (Microchip), Moritz von Dawans (ARIES Embedded)

Language: English

Prerequisites: None

Seminar Actions: Presentation

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Agenda (Time zone: CET)

09:00 – 09:10	Welcome
09:10 – 09:30	Introduction to Microchip's PolarFire [®] SoC family
09:30 – 10:20	Yocto & Embedded Linux Development flow with Microchip's PolarFire [®] SoC
10:20 – 10:30	Questions & Answers

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