



# Building Applications on multi-core RISC-V<sup>®</sup> based on Microchip PolarFire<sup>®</sup> SoC – Part III Advanced Security: Shielding against cyber attacks

8<sup>th</sup> December 2021 – online

Today's applications are expected to meet demanding functional requirements and to achieve highest security levels at a time.

Microchip PolarFire SoC FPGA with its multiple RISC-V 64-bit cores provide a solid foundation for all application security needs. Design security protects the design intellectual property (IP) and other sensitive information such as cryptographic keys that are used for the FPGA configuration. Data security protects application data—stored, communicated, or computed at run-time—from being copied, altered, or corrupted. PolarFire SoC FPGA devices have a dedicated crypto processor, referred as User Cryptoprocessor, for data security applications.

This webinar will introduce how one can design a secure environment with the PolarFire SoC asymmetric multi-core processor allowing a versatile mix of deterministic real-time security applications and the Linux OS in a single multi-core CPU cluster.

**Speakers:** Martin Kellermann (Microchip), Daire McNamara (Emdalo Technologies)

**Language:** English

**Prerequisites:** None

**Seminar Actions:** Presentation

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## Agenda (Time zone: CET)

09:00 – 09:10	Welcome
09:10 – 09:30	Introduction to Microchip PolarFire SoC secure architecture
09:30 – 10:20	Working with Microchip PolarFire SoC in secure environments
10:20 – 10:30	Questions & Answers

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